Innovation XR Training Platform

Reducing Fabricated Semiconductor Training Costs and Enhancing Reality

XR Solution Proposal for Semiconductor Education Innovation



SEMICONDUCTOR Fabrication Training

XR N-FARADAY

PROPOSAL DECK · 2026

namo
NamoWebiz, Incorporated.

XR SEMICONDUCTOR

XR N-FARADAY

This proposal introduces a **"Virtual Reality-Based Semiconductor Education Solution"** for innovating semiconductor education. This virtual training platform is designed to elevate the quality of workforce development and maximize educational efficiency amid the rapidly changing semiconductor industry landscape, strengthening trainees' ability to adapt to real-world work environments.

This solution is designed for **university professors** who operate or are planning semiconductor manufacturing training programs, and for **K-12 teachers** who wish to introduce career-experience programs. By providing an immersive educational experience similar to actual field conditions, it contributes to cultivating future semiconductor specialists and helps build a sustainable education ecosystem through a systematic curriculum.

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2025: Expanding Investment in Semiconductor Talent Development

The semiconductor industry is the core foundation for advanced technologies such as artificial intelligence (AI), autonomous driving, and 5G, and global demand is surging. Despite this growth, Korea's semiconductor industry faces a severe workforce shortage, with demand expected to nearly double the current workforce by 2031.

Government Budget

2025 Budget Finalized

Ministry of Science and ICT — KRW 18.9 Trillion

KRW 26.5T → 29.7T

+KRW 3.2T (+11.9%) YoY R&D budget

Talent Program

Industry–Academia AI Semiconductor Talent Program

Cultivating top-tier AI semiconductor talent through 6-year industry-academia research projects.

~370 / yr

Advanced-degree specialists trained annually

Ecosystem Support

Government to Expand Semiconductor Ecosystem Support

Support for equipment and materials/parts firms scaled up nationwide.

KRW 26T → 33T

Total semiconductor ecosystem support

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Semiconductor Processes Are High-Risk — Lab Safety Is a Growing Concern

NEWS

Sulfuric Acid Explosion at Hanyang University Lab Injures 4 Students

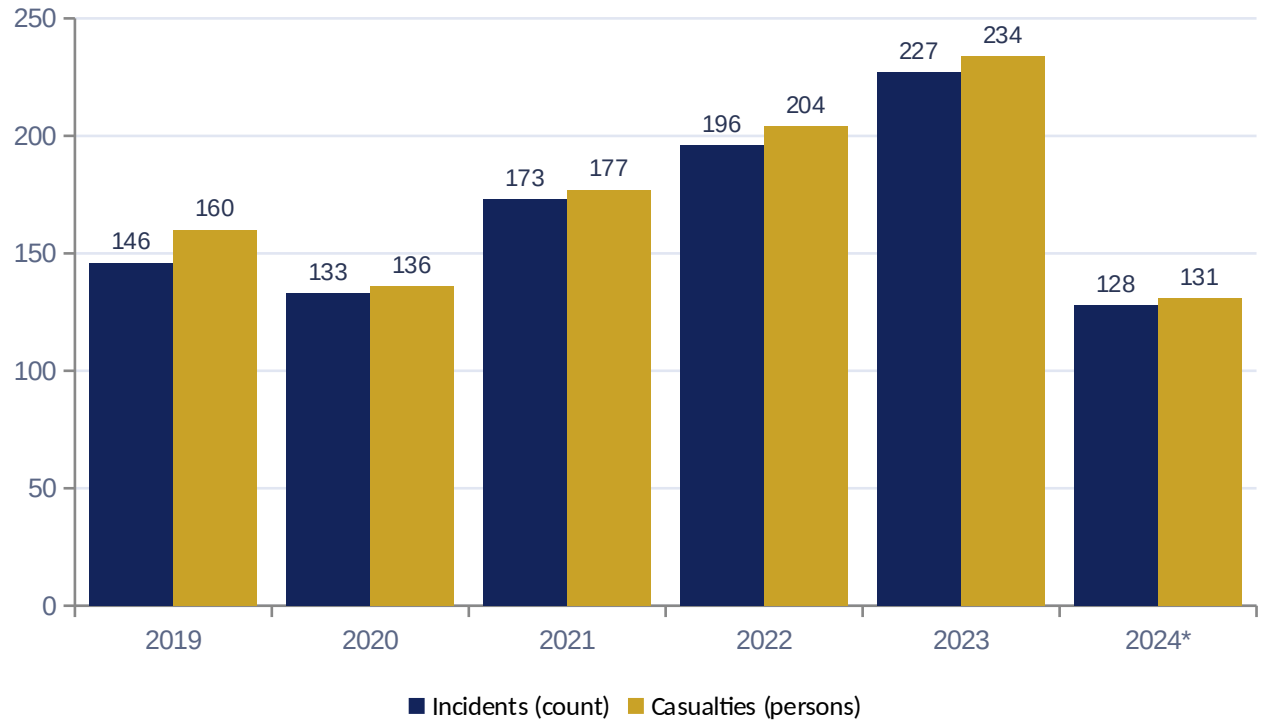
April 9, 2025 — Graduate students were conducting an experiment when leaking sulfuric acid ignited a fire in the lab.



University research-lab safety incidents keep rising as hands-on training expands without matching safety infrastructure — reinforcing the need for a safer way to train.

University Lab Safety Incidents

**2024 figures through August*



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Building Semiconductor Manufacturing Equipment Costs KRW 2 Billion+

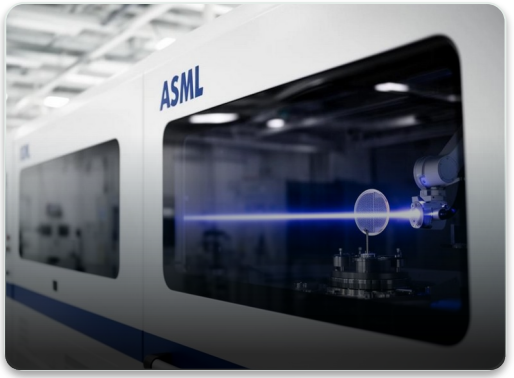
Real facilities such as cleanrooms and semiconductor vacuum equipment require an actual build cost ranging from hundreds of millions to tens of billions of KRW.



Cleanroom fabrication facilities require tightly controlled environments and specialized equipment.

NEWS

'EUV Lithography Machine' Tops KRW 500 Billion per Unit — ASML Unveils Next-Gen Pricing



NEWS

“Taiwan's TSMC Reportedly Buys ASML's Next-Gen EUV Equipment for KRW 522 Billion per Unit”

Reported to be the industry's first next-gen EUV tool purchase among foundries.



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Why Is Semiconductor Workforce Development Stalling?

Despite active government support for semiconductor talent development, chronic problems in the education field are causing a serious workforce shortage — a threat that undermines the very foundation of national semiconductor competitiveness.

01



Curriculum Disconnected from the Field

✓ Only 42% of courses reflect industry requirements

✓ Outdated content fails to keep pace with fast-changing tech trends

02



Absolute Shortage of Specialized Educators

✓ Too few professors with real industry field experience

✓ Talent concentration in the capital region lowers teaching quality at regional universities

03



Poor Training Infrastructure

✓ Budget shortfalls mean under 30% hold essential equipment

✓ Costly, outdated equipment leads to theory-only, “half-baked” education

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The Fatal Barrier to Hands-On Training: Astronomical Equipment Cost

The semiconductor industry's core competitiveness depends on talent with hands-on skills operating real process equipment. Yet most universities struggle to build genuine hands-on training environments, ultimately producing "half-baked education" where students learn processes only through slides and simulations — never touching or operating costly real equipment.

Equipment Prices Beyond Imagination

Core equipment for the 8 major semiconductor processes (lithography, deposition, etching, etc.) costs from hundreds of millions to hundreds of billions of KRW per unit. Even downgraded or used training equipment costs at least hundreds of millions of KRW — impossible within limited budgets.

Maintenance Costs Bigger Than the Purchase Itself

Beyond acquisition cost, cleanroom facility upkeep, specialty gas/chemical supply, parts replacement, and specialized engineer labor generate enormous annual maintenance expenses.

Universities' Practical Limits

Even after some equipment is introduced through government-funded projects, continuous operation and upgrades remain limited. Outside a handful of large national shared research facilities (fabs), most universities hold only 1–2 units of core process equipment.

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Lack of Content Is Driving Down the Quality of Semiconductor Education

Beyond the equipment shortage, the absence of curricula and standardized teaching materials that keep pace with industry change is another critical barrier. This deficiency in “what to teach” deepens the gap between theory and practical skill — directly undermining national talent competitiveness.

01



Curriculum Rigidity

✓ Lack of Content Is Driving Down the Quality of Semiconductor Education

✓ Skills companies actually need nanoscale process practice, packaging technology, data analysis are neglected in curricula

02



Lack of Supplementary Materials

✓ Beyond basic textbooks, there is an absolute shortage of hands-on exercises, simulation toolkits, and problem-based-learning (PBL) materials built on real industry data

✓ This deprives students of the chance to apply theory and build problem-solving skills

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A Training Program Free of Location, Cost, and Content Constraints Is Needed

Adopting XR/VR technology in semiconductor education goes beyond a simple change of tools — it is building a new educational paradigm. It replaces expensive equipment, reduces the risk of hands-on training, removes spatial constraints, and maximizes educational impact.

Namo Webiz's “Virtual Reality-Based Semiconductor Education Solution” responds to this era's demands — resolving the semiconductor industry's workforce shortage while leading educational innovation across future advanced-industry fields.

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Accuracy Developed by Industry Professionals

Training Content Developed with Industry-Leading Expertise

29 People

Semiconductor experts directly involved (avg. 10+ yrs experience, PhD holders)

- ✓ Process reproduced from real field experience
- ✓ Proven educational effectiveness meeting industry standards
- ✓ Continuous update & improvement system

Existing Solutions

Low-quality 3D modeling, inaccurate process information, poor user experience

This Proposal

Verified by field experts, precise process reproduction, intuitive interface

Content Quality Assurance

- ✓ Stage-by-stage expert review & approval
- ✓ Verified match with real fab environments
- ✓ Continuous update & improvement system
- ✓ Reviewed by professionals from Company S and Company H

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High-Quality Realism

Next-Gen XR Technology and High-Quality 3D Modeling for Immersive Learning

90%+

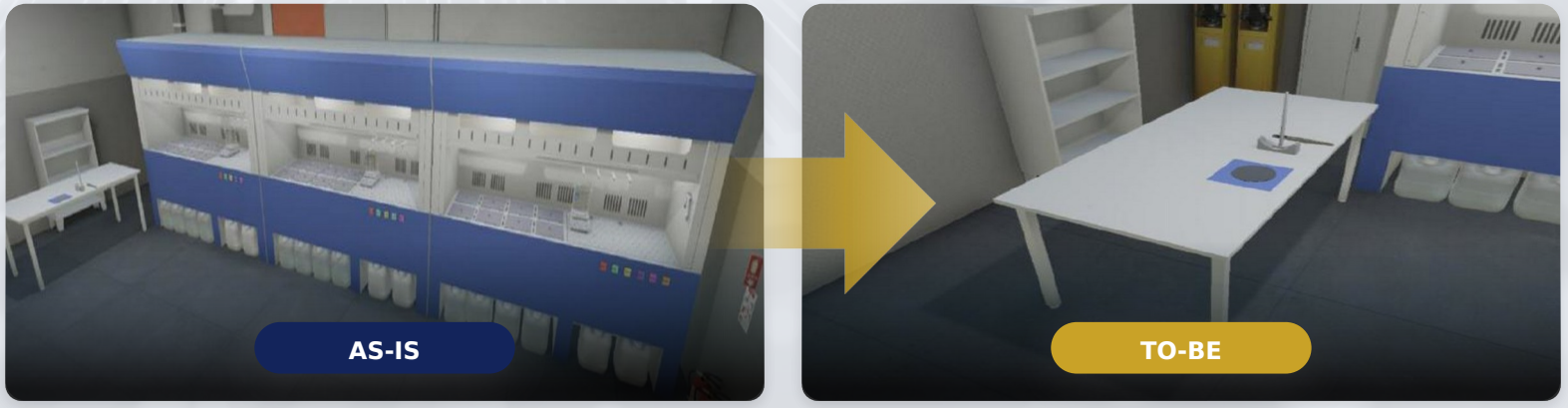
3x
Focus improvement

80%+
Info. retention

Learning safety 100%

Immersive Learning Specs

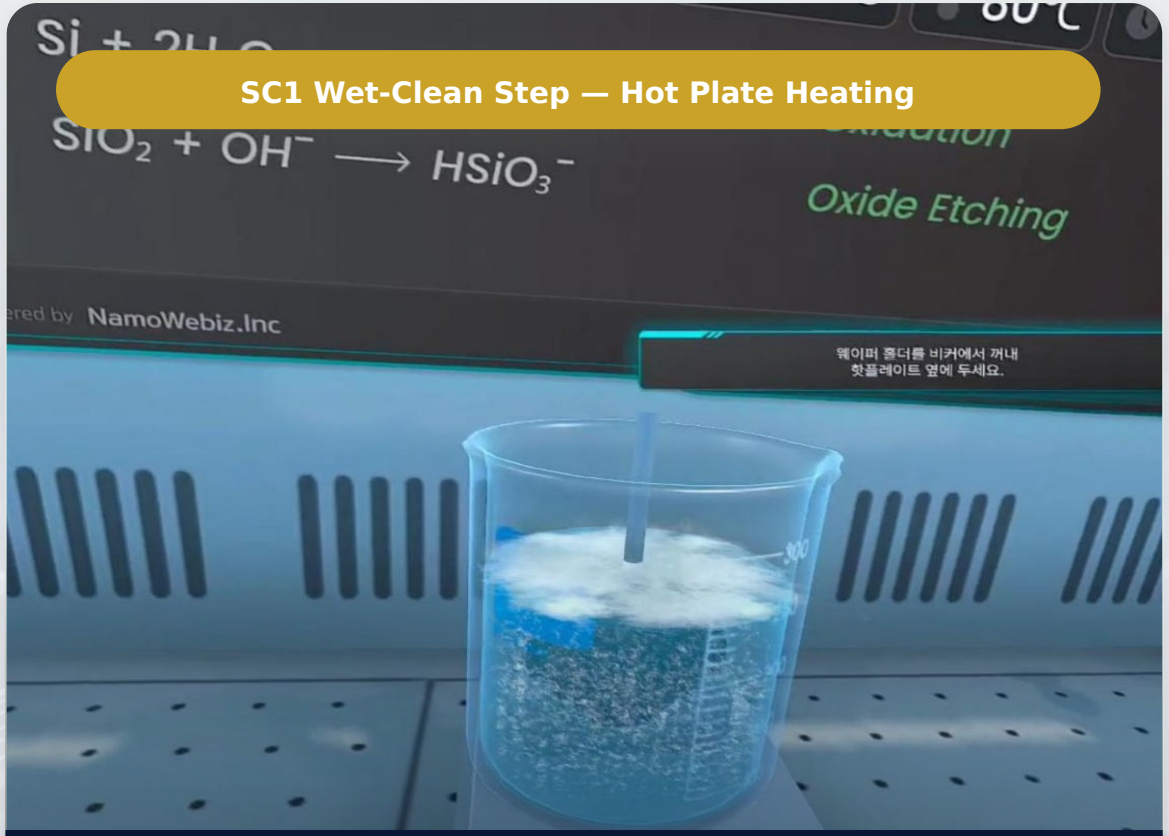
- Ultra HD resolution (4K+)
- Real-time rendering (60FPS+)
- Precise physics simulation
- 3D spatial audio



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Learning Effectiveness Boosted Through Interaction

SC1 Wet-Clean Step — Hot Plate Heating



✓ **Animation is applied dynamically as temperature changes**

Furnace Temperature Control



- ✓ Trainee sets the target furnace temperature
- ✓ Animation reflects the furnace as temperature rises
  (e.g. 1 min · 1100°C)

✓ **Real-time reactive process feedback**

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Reactive, Branching Condition Scenarios

Strengthening Practical Skills Through Real-Time Interaction

100+

Real-time | Instant | Tailored

✓ Dynamic process-branching scenario system
✓ Real-time result display & analysis

✓ Adaptive, personalized learning paths
✓ Instant error alerts & correction

Automated Learning Engine

✓ AI-based learning analytics engine
✓ Real-time process simulation module
✓ Individual learning history tracking
✓ Performance evaluation & recommendations



Wet-Bench Process Console

Trainees enter process parameters (speed, temperature, time) directly on the in-VR keypad; the system validates the setpoints and shows real-time results before allowing the process to proceed.



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A Detailed, Field-Focused Curriculum

A Systematic, Practice-Oriented 16-Week Training Program

16 Weeks	64 Hours	MOSFET
Total training program	Total training time	Manufacturing-focused practice

Weeks 1–2
Semiconductor fundamentals & VR system orientation

Weeks 3-6
Wafer preparation & basic processes

Weeks 7-10
Photolithography & etching processes

Weeks 11-14
Ion implantation & metallization processes

Hands-On Skill Building

- ✓ 100% real-equipment simulation reproduction
- ✓ Field-recipe-based process optimization
- ✓ QC methods incl. SPC and yield analysis
- ✓ Practical Auto CAD mask design

Assessment & Certification

- ✓ Stage-by-stage theory/practical evaluation (70%+)
- ✓ Final project: MOSFET design
- ✓ Certificate of completion issued
- ✓ Field-linked career support

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An Economical Solution

Maximizing Training ROI Through Outstanding Cost Efficiency

~5%

of real equipment build cost achieves 85%+ of the training effect

KRW 5.0 B
Real fab build

KRW 250 M
VR solution

90%
Annual opex savings

0%
Safety incident risk

∞
Repeatable practice

12-Inch Wafer Equipment Reproduction

- ✓ Industry-standard specification (current mainline production)
- ✓ Real process environment reflected
- ✓ Precise temperature, pressure, gas-flow reproduction
- ✓ Latest fab equipment (as of 2023)

Where the Savings Come From

- ✓ Avoids the cost of chemical & high-temperature equipment accidents
- ✓ Removes concern over damaging costly equipment
- ✓ Saves facility build-out cost through remote training

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Integrated Management & Scalability

Maximizing Efficiency with a Smart Training-Management System

LMS Integration

- ✓ Real-time learning-progress monitoring
- ✓ Automated assessment system
- ✓ AI-based personalized feedback
- ✓ Individual learning analytics & recommendations

Integrated Control System

- ✓ Centralized equipment management
- ✓ Remote power ON/OFF control
- ✓ Bulk content distribution
- ✓ Real-time monitoring

Scalability & Compatibility

- Modular Design
- Multi-Platform
- Cloud Infrastructure

Data Analysis & Utilization

- ✓ Centralized equipment management
- ✓ Content distribution & live monitoring

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Tailored, Integrated Programs by Level

The core product is “Namo N-View for Semicon” (XR Semiconductor Manufacturing Education Platform). It reproduces the full semiconductor manufacturing process using VR/AR/XR technology, and is delivered as an integrated program including level-specific curricula — not just training content.

University

Covers core subjects — semiconductor devices, processes, materials, and circuit design — combining theory and hands-on training.

- ✓ N-View for Semicon platform
- ✓ Standard 2-semester (16-week) curriculum
- ✓ MOSFET-based 50-step process practice
- ✓ Digital/analog circuit design, device & process theory, design-rule learning
- ✓ In-depth teaching materials & detailed assessment resources

K–12

Scenario-based content focused on sparking interest and career exploration, including cleanroom basics and equipment-maintenance practice.

- ✓ N-View for Semicon experience content
- ✓ NCS-based scenarios for cleanroom & equipment disassembly/assembly experience
- ✓ Simplified teaching materials & assessment resources

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A Modular Program — Adopt Only What You Need

Software

N-Faraday XR

Reactive process simulation, XR content management, LMS-linked learning & assessment

Integrated LMS

Progress tracking & live assessment, content selection/distribution, remote power control

Process-Specific Content

Equipment disassembly/assembly, cleanroom use & safety-incident-response content

Hardware

HMD — Meta Quest 3 | **PC — RTX 3080+** | **Server — Cloud**

Education Program

Textbooks & Supplementary Materials

Beginner-to-expert learning materials, incl. Vietnamese-language UI and English/Vietnamese supplements

Assessment Materials

Built-in learning-progress tracking & real-time evaluation

Instructor Training Program

Support for solution usage and teaching-method development

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Proven Track Record Across Industries and Education

CASE 01

“Cheongnyeon Hy-Five” Program

VR-based semiconductor process practice was incorporated into the specialized track of this future-talent program. Trainees praised the process detail as remarkably close to the real field — highly useful for building process understanding and confidence.

CASE 02

Daegu Meister High School

To replace costly, hazardous equipment, a virtual training content project recreated a semiconductor fab cleanroom tour. Component-type (disassembly/assembly) and scenario-type (sequential steps, emergency response) content boosted training effectiveness.

CASE 03

Samsung DS Division — Environment & Safety Academy

VR safety training content for employees and partner-company workers contributes to industrial-accident prevention, recreating cleanroom fires, forklift collisions, and falls from height for realistic safety training.

CASE 04

Hanoi International College / Hanoi University of Industry

Content introduced for semiconductor equipment maintenance and repair training.

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Key Benefits and Value Proposition of XR-Based Training

01

Improved Training Quality & Efficiency

✓ **Stronger practical field skills**
Repeated practice in a near-real virtual environment greatly improves job proficiency and field adaptability

✓ **Maximized immersion & learning effect**
High-quality XR increases immersion, encouraging retention and proactive, self-directed learning

✓ **Safe training environment**
Hazardous, high-cost processes and emergency scenarios can be trained safely without real equipment

02

Optimized Use of Training Resources

✓ **Dramatic cost savings**
Substantially reduces spending on equipment, maintenance, consumables, and cleanroom operation

✓ **Removal of spatial constraints**
Hands-on training without large equipment or a cleanroom reaching more students

✓ **Flexible curriculum operation**
Diverse scenarios enable tailored education; content updates and reuses quickly

03

Future Talent Development & Competitiveness

✓ **Supplying industry-matched talent**
Repeated practice in a near-real virtual environment greatly improves job proficiency and field adaptability

✓ **Expanded educational access**
Low-cost delivery expands quality training access to all interested students

✓ **Promoting interdisciplinary convergence**
Opens a path for fields like mechanical engineering into semiconductors

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Partner Organizations



Namowebiz XR Platform

한국에너지기술연구원 KOREA INSTITUTE OF ENERGY RESEARCH

KRICT 한국화학연구원

Northwestern University

UNIST ULSAN NATIONAL INSTITUTE OF SCIENCE AND TECHNOLOGY

한국폴리텍대학 부산캠퍼스

Produced in Collaboration with Specialized Research Institutions

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XR N-FARADAY

Namo Webiz's ‘Virtual Semiconductor Manufacturing Training Solution’ goes beyond simply adopting the latest technology — it overcomes the fundamental limits of semiconductor education and sets a new standard for future workforce development.

By converting a costly, high-risk training environment into a safe and efficient virtual space, we provide students with field-realistic hands-on experience and give institutions a sustainable training infrastructure. Through this proposal, universities can strengthen their role as semiconductor talent-development centers, and K-12 schools can offer students meaningful career-exploration opportunities — together building a solid talent foundation for the continued growth of Korea's semiconductor industry.

Thank You

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PC & Device Specifications

Compare hardware specifications

PC Specifications

Component	Requirement
CPU	11th Gen Intel(R) Core(TM) i7-11800H @ 2.30GHz
RAM	16.0GB
Software	Windows 10 Pro
GPU	NVIDIA GeForce RTX 3080 Laptop GPU
Storage	SDD 1TB

Meta Quest 3

Component	Specification
Processor	Qualcomm Snapdragon XR2 Gen 2
RAM	8 GB LPDDR5 SDRAM
Memory	128 / 512 GB UFS 3.1 Internal Memory
Display	2 x 2064 x 2208 / 90-120Hz Fast-switch LCD
Lens	Pancake Lens

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Semiconductor Manufacturing Process

Approximately 40+ Process Steps

1 Step group 1 of 3

Steps 01-07

R.C.A. Cleaning					Furnace	CVD
Pre-cleaning →	SC1 →	SC2 →	Piranha →	HF & Rinsing →	SiO_2 Deposition →	SiNx Deposition

Steps 08-15

Photo-Lithography (Mask #1)				Sputter	CVD	Furnace
P.R. Coating →	U.V. Exposure →	Developer →	Rinsing →	R.I.E. (Etching) →	PR Remove →	PR Remove →	SiO_2 Deposition

Steps 16-22

Photo-Lithography (Mask #2)				Sputter	CVD
P.R. Coating →	U.V. Exposure →	Developer →	Rinsing →	R.I.E. (Etching) →	PR Remove →	N++ Doping

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Semiconductor Manufacturing Process

Approximately 40+ Process Steps

2 **Step group 2 of 3**

Steps 23-29

Photo-Lithography (Mask #3)				Sputter		CVD
Pre-cleaning →	U.V. Exposure →	Developer →	Rinsing →	R.I.E. (Etching) →	PR Remove →	N++ Doping

Steps 30-36

Furnace	Photo-Lithography (Mask #4)				Sputter	
SiO_2 Deposition →	P.R. Coating →	U.V. Exposure →	Developer →	Rinsing →	R.I.E. (Etching) →	PR Remove

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Semiconductor Manufacturing Process

Approximately 40+ Process Steps

3 Step group 3 of 3

Steps 37–43

Photo-Lithography (Mask #5) → P.R. Coating → U.V. Exposure → Developer → Rinsing → **Sputter** → R.I.E. (Etching) → PR Remove → **Evaporator** → Metallization (Al/Cu)

Steps 44–49

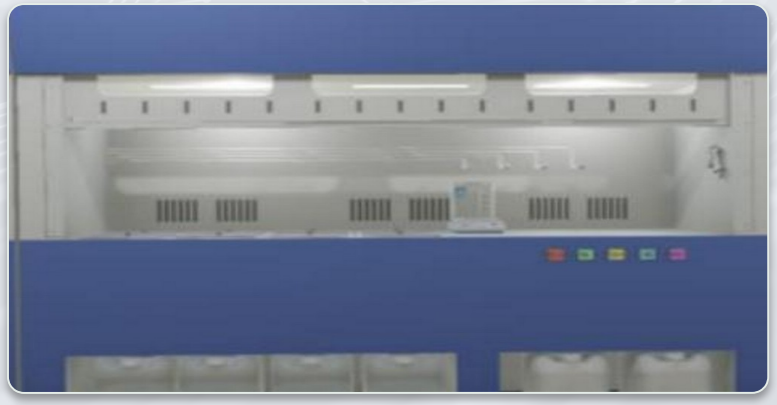
Photo-Lithography (Mask #6) → P.R. Coating → U.V. Exposure → Developer → Rinsing → **Fume Hood** → Wet Etching → PR Remove

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R.C.A Cleaning Room





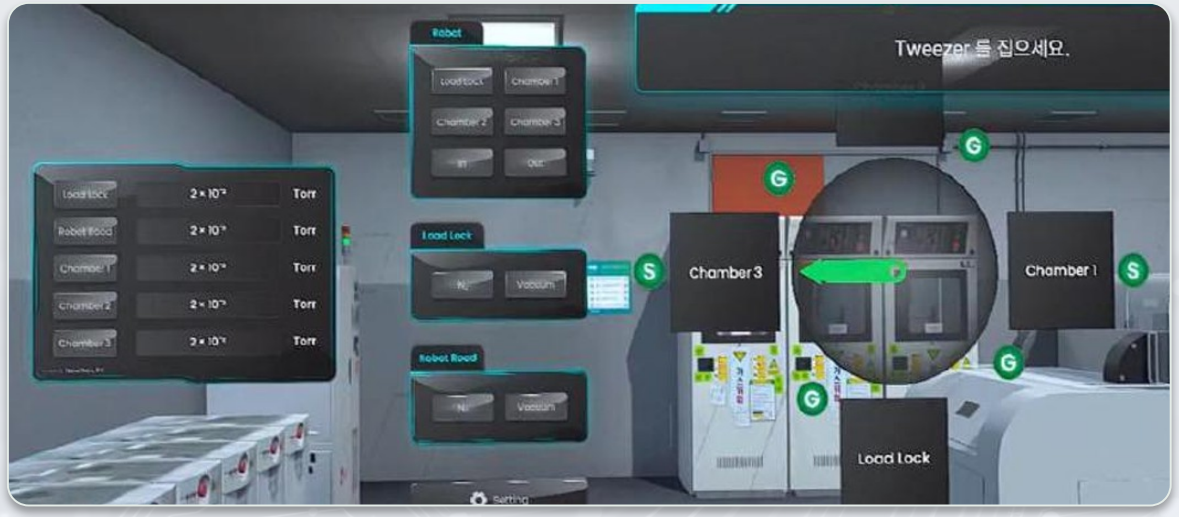






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Cluster Room





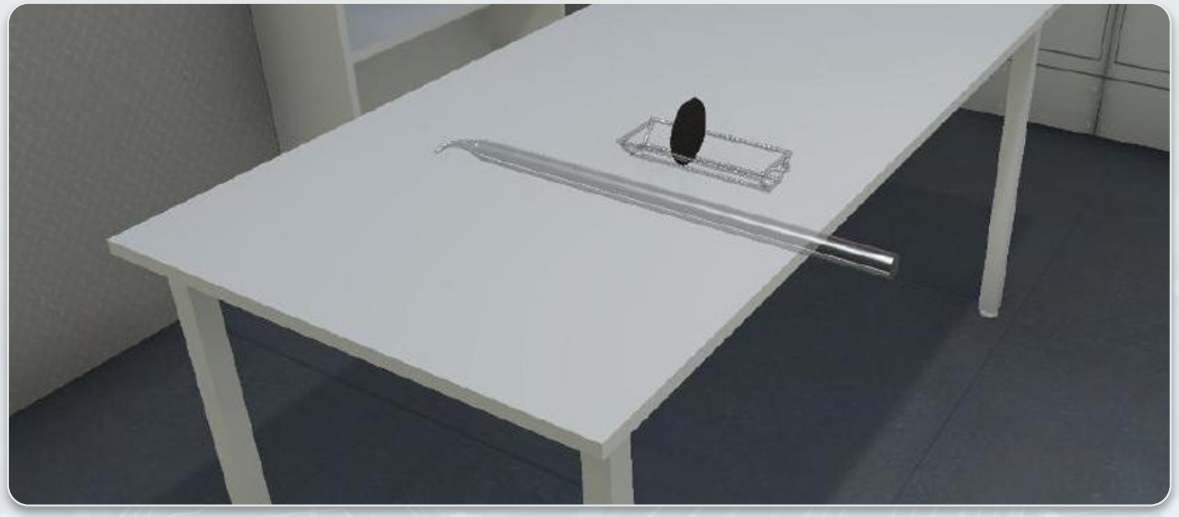






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Furnace Room











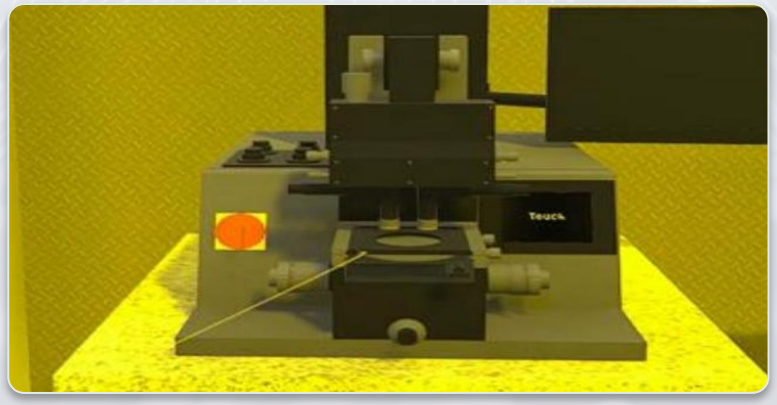
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Lithography Room







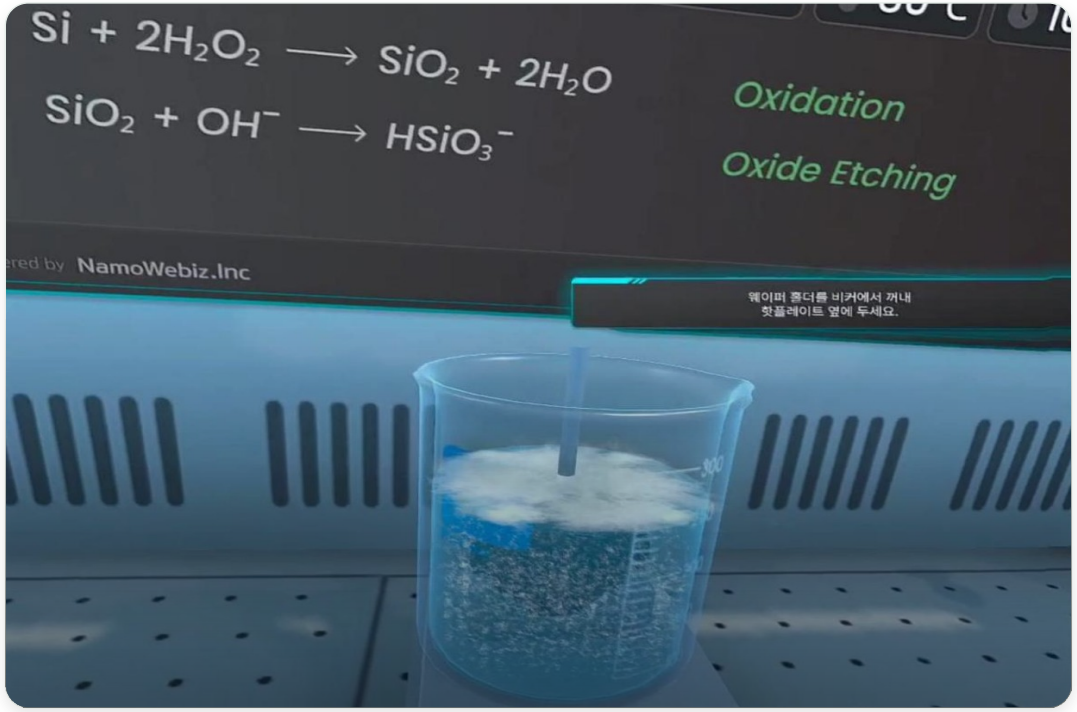




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Reaction Simulation Video

SC1 Wet-Clean Step



On-Screen Chemistry

Oxidation

✓ $Si + 2H_2O_2 \rightarrow SiO_2 + 2H_2O$

Oxide Etching

✓ $SiO_2 + OH^- \rightarrow HSiO_3^-$

On-screen guidance (translated)

“Take the wafer holder out of the beaker and place it next to the hot plate.”

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